

## PATENT ABSTRACTS OF JAPAN

(11) Publication number : 02-135880  
 (43) Date of publication of application : 24. 05. 1990

(51) Int. CI. H04N 5/335  
 H04N 5/225  
 H04N 5/232  
 // H04N 5/228

(21) Application number : 63-289297 (71) Applicant : MATSUSHITA ELECTRIC IND CO LTD  
 (22) Date of filing : 16. 11. 1988 (72) Inventor : KOJIMA ICHIRO  
 MORIMURA ATSUSHI

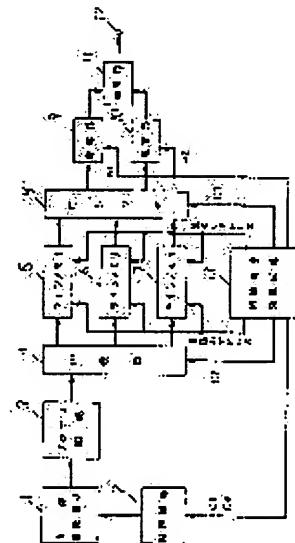
## (54) IMAGE PICKUP DEVICE

## BEST AVAILABLE COPY

## (57) Abstract:

PURPOSE: To attain the enlargement processing of a picture with less memories by weighting the output signal of a selected line memory, adding it and interpolating a new scanning signal.

CONSTITUTION: A control signal C1 given from a control signal generation circuit 13 controls the vertical scanning of a solid-state image pickup element 1 and expands the picture in a vertical direction. M-number of multipliers 9 and 10 weight the output signals of m-number of line memories 5-7 ( $2 \leq m \leq n$  and (m) is an integer) which a selector 8 select among line memories M1-Mn ( $n \geq 3$  and (n) is the integer), and an adder 11 performs addition so as to interpolate the new scanning signal. Thus, the picture can be enlarged and interpolated only by less line memories.



## LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's

decision of rejection]

[Date of extinction of right]

Copyright (C) ; 1998, 2003 Japan Patent Office